## APPARATUS OF SEALING WAFER BACKSIDE FOR FULL-FACE PROCESSING Ashjaee et al. Appl. No.: 10/788,926 Atty Docket: ASMNUT.092C1

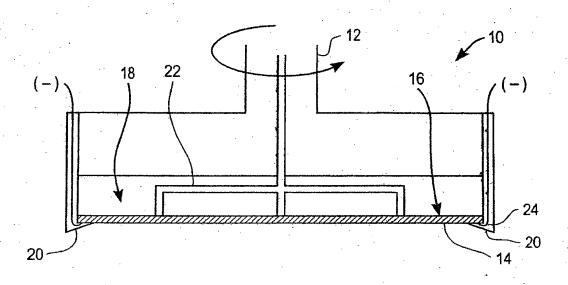


FIG. 1A (PRIOR ART)

#### $\begin{array}{c} \textit{APPARATUS OF SEALING WAFER BACKSIDE FOR FULL-FACE} \\ \textit{PROCESSING} \end{array}$

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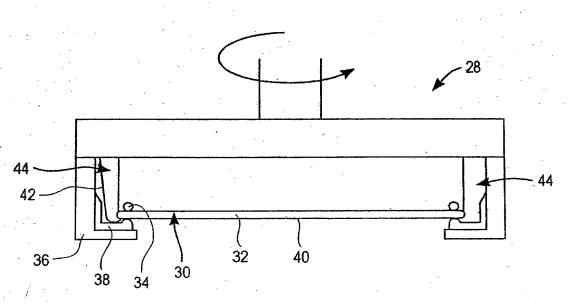


FIG. 1B (PRIOR ART)

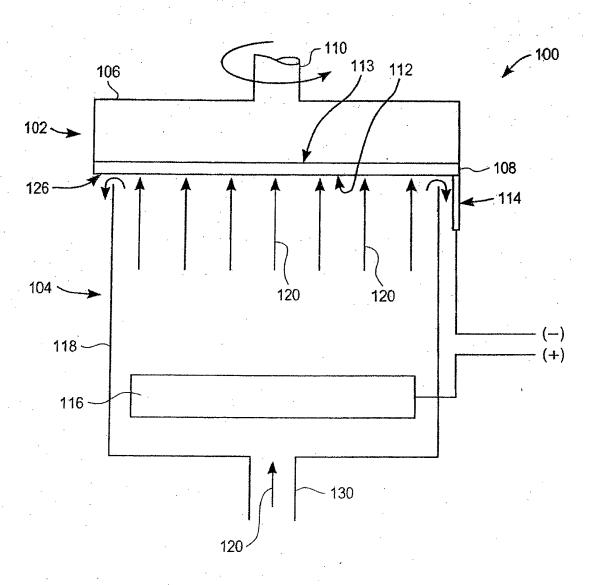


FIG. 2

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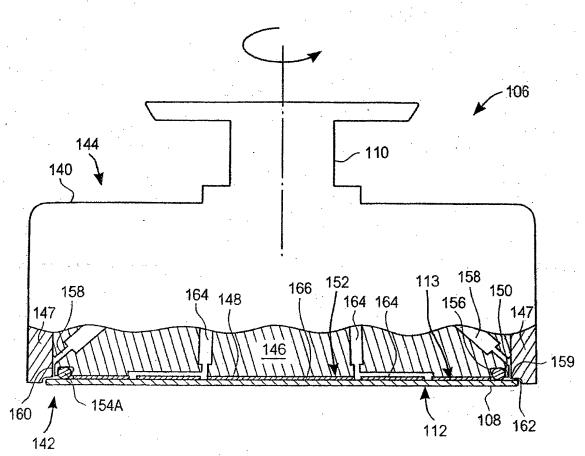


FIG. 3A

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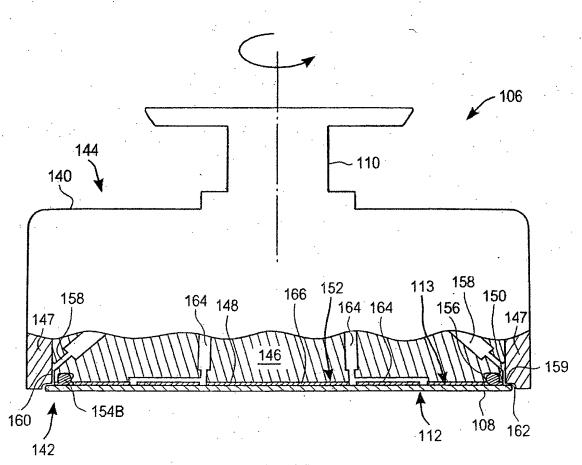


FIG. 3B

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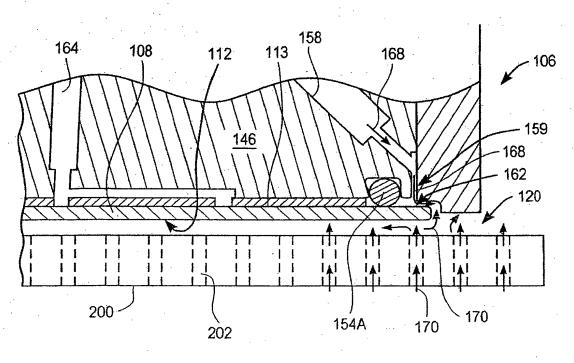


FIG. 4A

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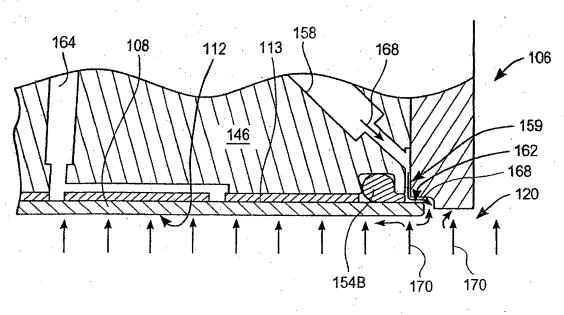


FIG. 4B

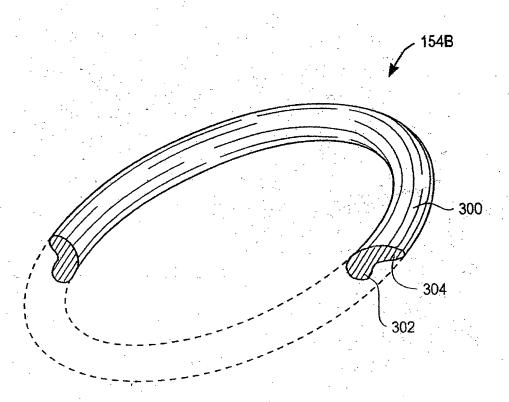


FIG. 5

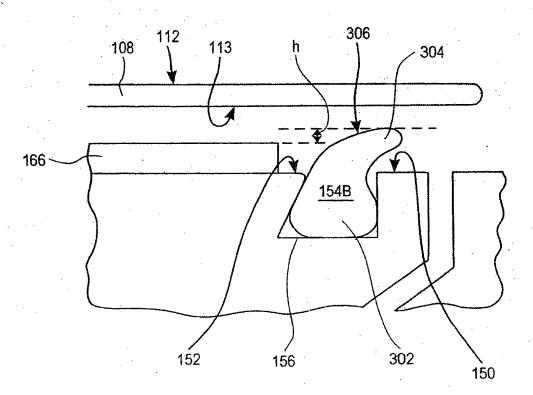


FIG. 6A

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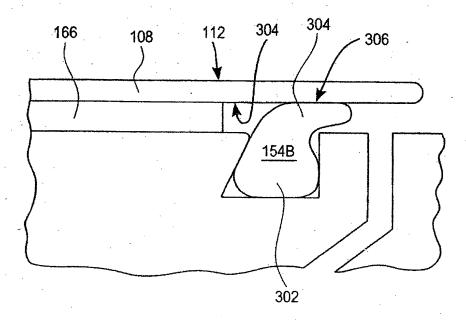


FIG. 6B

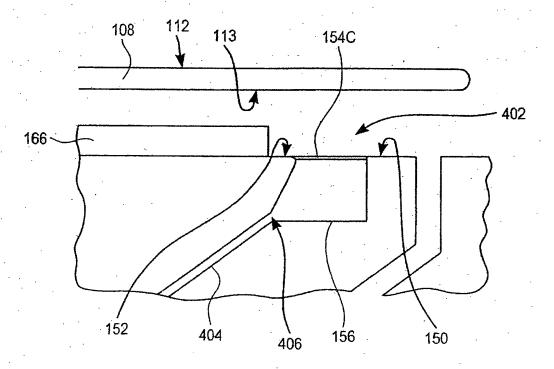


FIG. 7A

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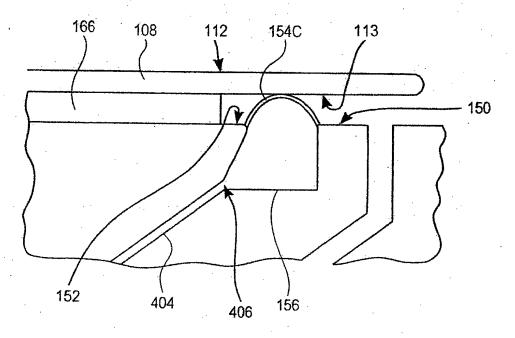


FIG. 7B

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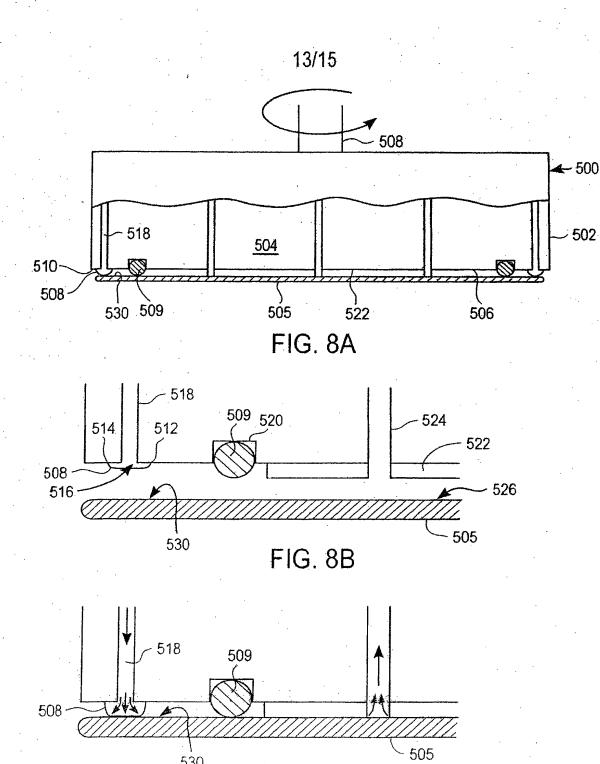


FIG. 8C

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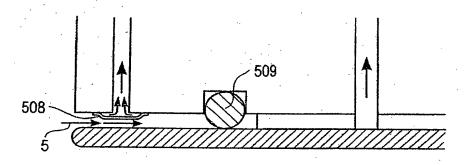


FIG. 8D

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